

## Dual limit comparator using single op-amp

This circuit was designed to give a positive output when the input voltage exceeded plus or minus 8.5 volts. Between these limits the output is negative. The positive limit point is determined by the ratio of  $R_1$ ,  $R_2$ , and the negative point by  $R_1$ ,  $R_3$ . The forward voltage drop across the diodes must be allowed for. The output may be inverted by reversing the inputs to the operational amplifier. The 709 is used without frequency compensation.

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