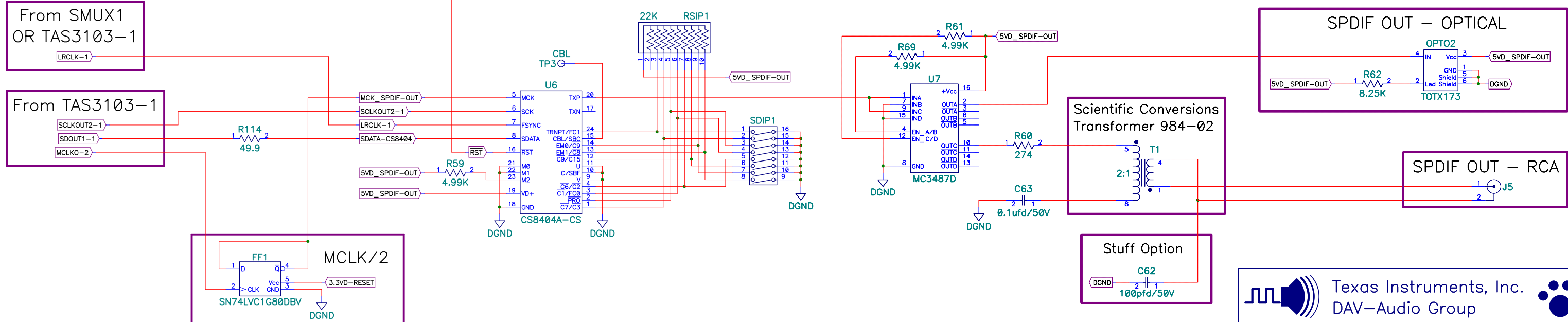


SPDIF IN SPDIF OUT



From SMUX1/TAS3103-1

From TAS3103-1


Line IN
Chan. 1

Line IN
Chan. 2

Line IN
Chan. 3

PCM1802 ADCs

TO
MUXES



Texas Instruments, Inc.
DAV-Audio Group

Project: TAS3103 Evaluation Module

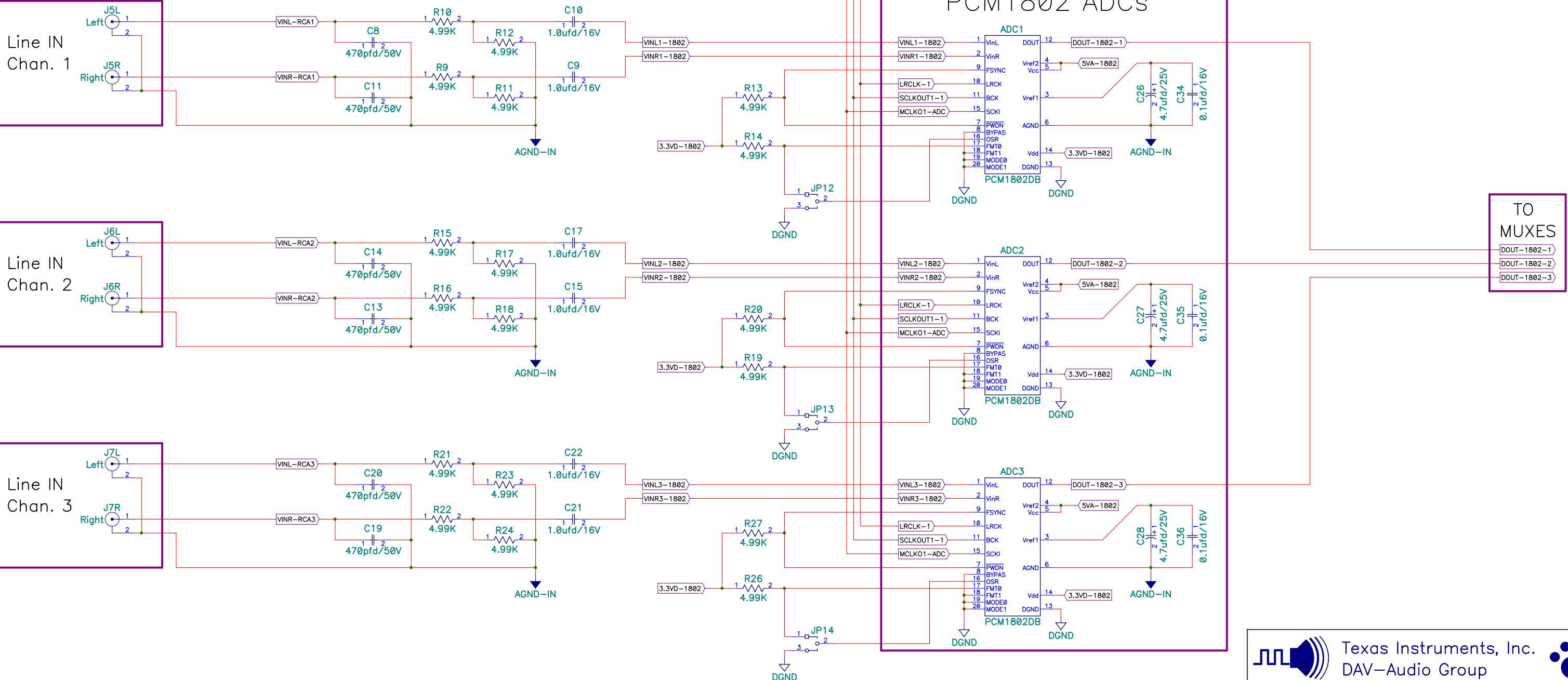
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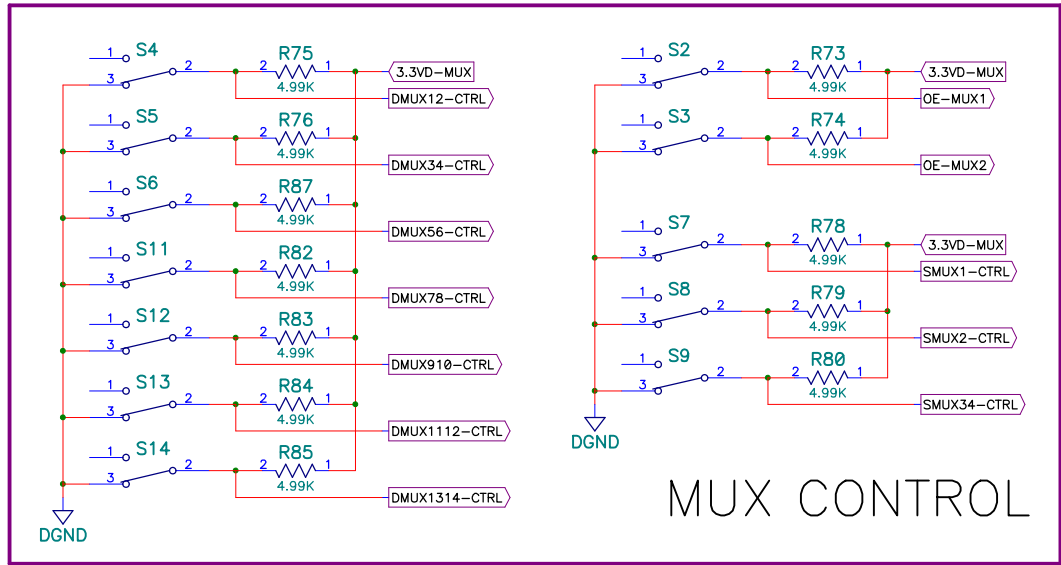
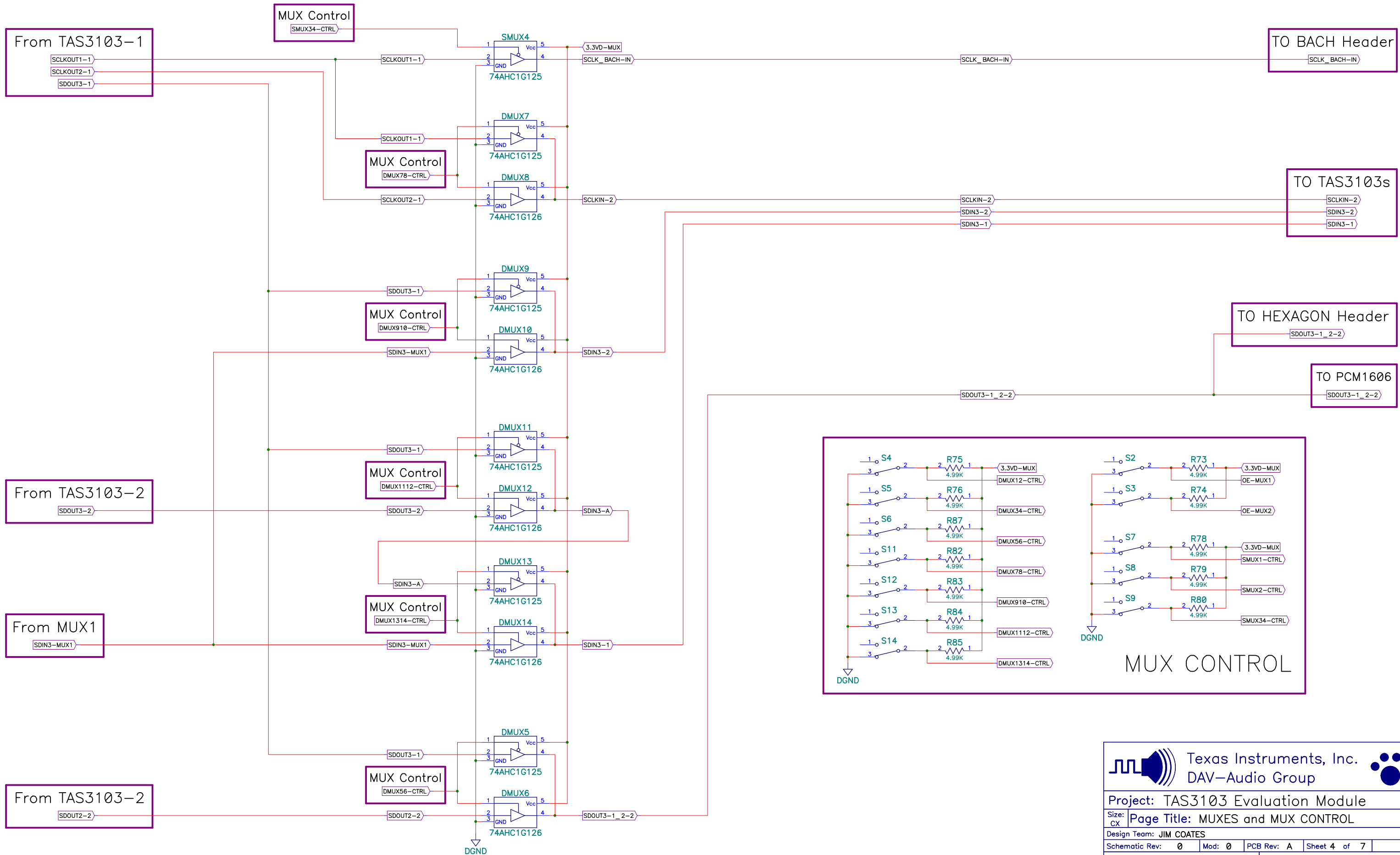
Design Team: JIM COATES

Schematic Rev: 0 | Mod: 0 | PCB Rev: A | Sheet 2 of 7

Save Date: Feb. 7, 2002 | Print Date: Tue Aug 06, 2002

Filename: TAS3103EVM.SCH | Drawn By: LDN





From DMUXES 1/2

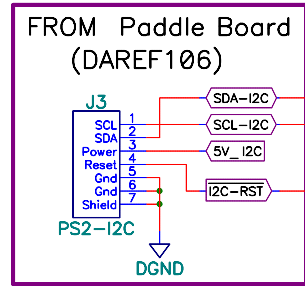
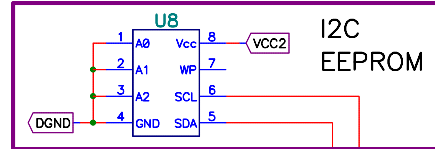
From MUX1

From DMUXES 13/14

From AND3

From MUX2

From SMUX1



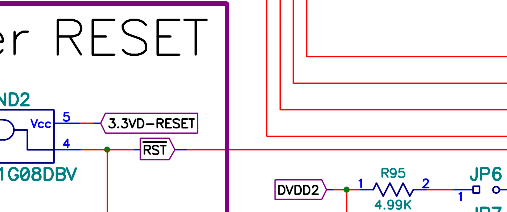
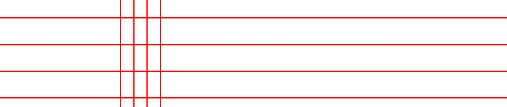
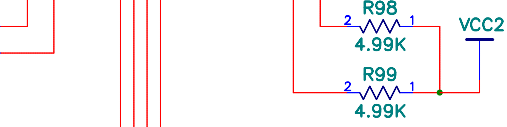
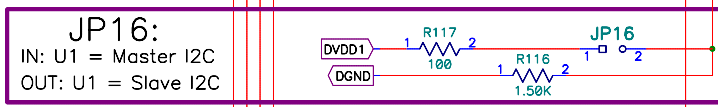
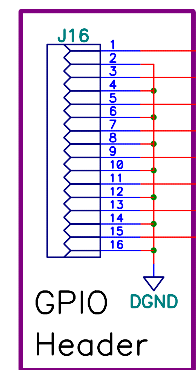
From DMUXES 9/10

From DMUXES 7/8

Push Button RESET

Master RESET

FROM BACH Header



From TAS3103-1

SDOUT1-1
SDOUT2-1
MCLKO-1
SCLKOUT2-1

From DMUXES 5/6

SDOUT3-1_2-2

From SMUX1
OR TAS3103-1

LRCLK-1

From TAS3103-2

MCLKO-2

From Master RESET

RST

From TAS3103-1

GPIO3-1
GPIO2-1
GPIO1-1
GPIO0-1

From Power Supply (VR6)

3.3VD-HXGN

24V Jack for Hexagon

J15

24V-HEXAGON

DGND

PCM1606 DAC

I2S OUTPUT HEADER

MCLK-HXGN
SCLK-HXGN
LRCLK-HXGN
SDOUT1-HXGN
SDOUT2-HXGN
SDOUT3-HXGN
IDC16
DGND

Hexagon
Board
Header

GPIO3-1
GPIO2-1
GPIO1-1
GPIO0-1
3.3VD-HXGN
24V-HEXAGON
1MM Flex24
DGND

