

# SCHEMATIC DIAGRAM-8

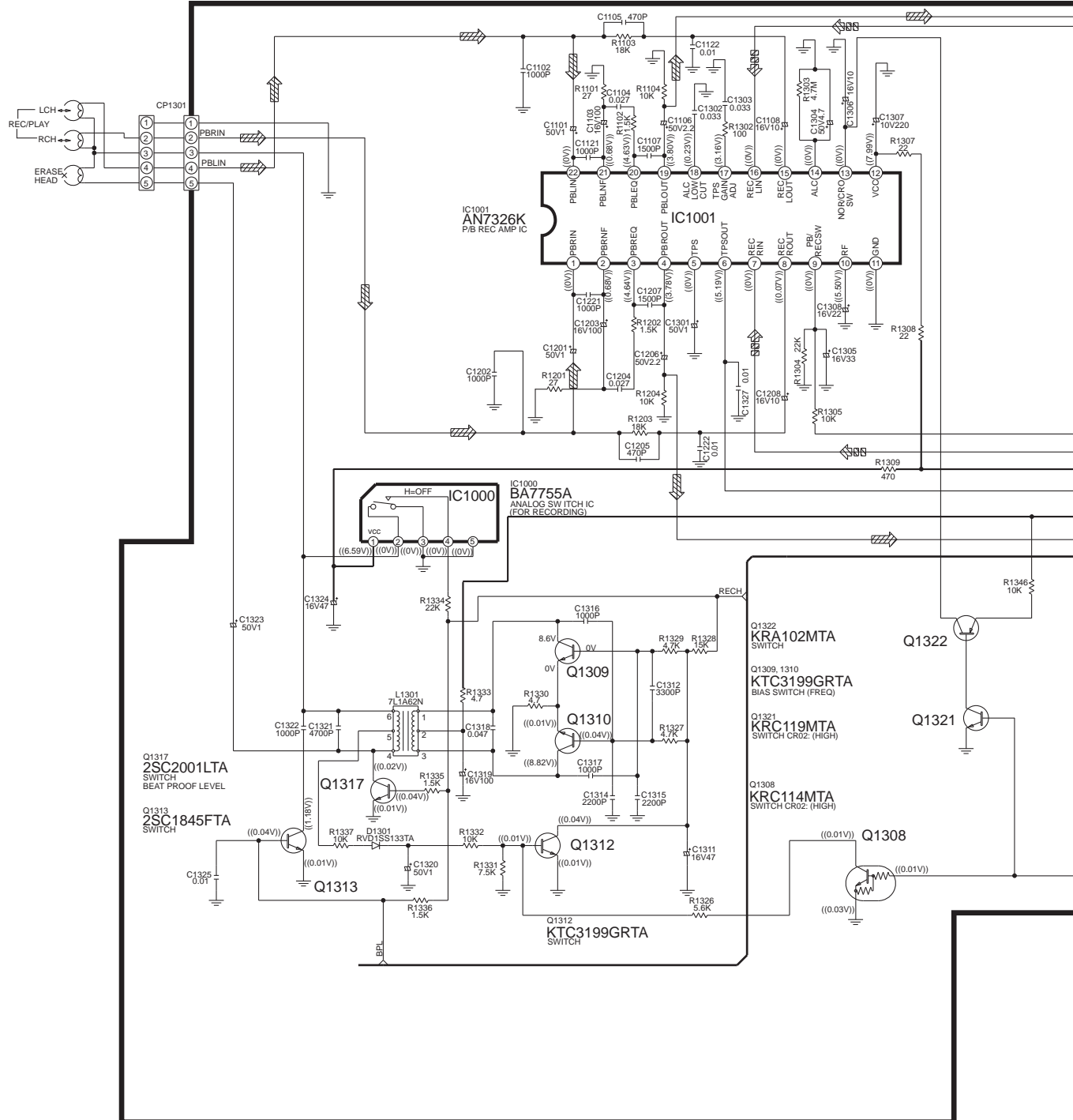
**G**

## CASSETTE DECK CIRCUIT

Record signal line

: +B signal line

Playback signal line



## H MECHANISM CIRCUIT

