



## Hex display

When experimenting on such projects as EPROM programmers and microcomputers, it is convenient to display the address and data buses in hexadecimal format. This circuit was designed to provide such a display.

The circuit is based on the Fairchild 9368 binary (hex) to 7-segment decoder/driver. The 555 oscillator clocks the 74LS93 which is wired to count to 6 in BCD, thereby selecting one digit at a time to be displayed on the multiplexed display. The 74145 decodes the outputs of the 74LS93 and drives the appropriate display cathode while the 4051

multiplexers select which four bits are sent to the 9368 via the 4050 buffers. The display can be any calculator-style 7-segment LED display (multiplexed, common-cathode).

An input voltage of about 0 to 2.5V represents a logic 0 while 2.5 to 5V represents a logic 1. The circuit uses a regulated supply of 5V at about 175mA.

For a somewhat larger display, try a 9369 in place of the 9368 and insert 68Ω current limiting resistors in series with pins 9-15. Any unused address lines should be tied to 0V.

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**\$20 for this item**