Programmable strobe

WHEN using magnitude comparators to evaluate the dynamic operation of digital counters, the output data for $A>B$, $A<B$, and $A = B$ is compared with a known conversion time for each bit by enabling exclusive OR gates with three separate strobe pulses. This circuit uses a monostable which, with the aid of three open collector inverting gates, will generate strobe pulses of 7.8µs, 10µs, 12µs, and a 15µs pulse to clear counters, etc.

The pulse widths can be increased or decreased by altering the value of C for the longest pulse and the three resistors in parallel for the three strobe pulses.
D. J. Greenland,
Cambridge.