

# designideas

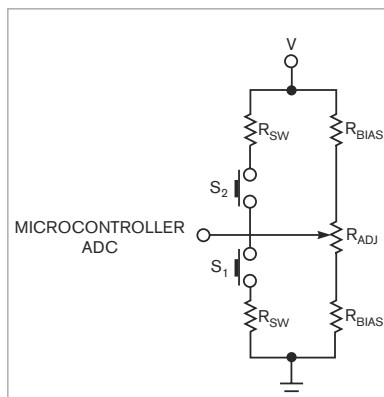
READERS SOLVE DESIGN PROBLEMS

## Read multiple switches and a potentiometer setting with one microcontroller input pin

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The circuit in this Design Idea provides a way to convey mixed analog and digital inputs into a microcontroller using one input pin. The output of the circuit connects to a microcontroller's ADC-input pin. The circuit comprises a single variable resistor and a number of SPST (single-pole/single-throw) switches (Figure 1). The push-buttons allow the user to select modes, states, or options, and the analog input provides a method of conveying an adjustable parameter. The implementation requires you to analyze a parallel resistor circuit and a voltage divider. If you carefully select the resistor values, the circuit provides a discernible analog input as well as a number of discrete pushbutton-input states.

Selecting the resistor values is a



**Figure 1** This circuit allows one microcontroller pin to read multiple switches and a potentiometer value.

multistep process, and a spreadsheet, which you can download at [www.edn.com/100422dia](http://www.edn.com/100422dia), aids in performing the calculations. Say, for example, that you want 5-k $\Omega$  potentiometer  $R_{ADJ}$  to produce a 0 to 100% value into the microcontroller. Typically, you would map the sampled value of 0 to 255 into a 0 to 100 value to represent a percentage. However, by selecting the values of bias resistor  $R_{BIAS}$ , you arrive at a direct analog input centered on the 0 to 255 range of the ADC—for example, 78 to 178.

To compute the appropriate high- and low-side bias-resistor values, the following equations solve this circuit as a simple voltage divider:

$$V_{LOW} = \frac{R_{BIAS}}{R_{ADJ} + 2 \times R_{BIAS}} \times V_{MAX};$$

$$V_{HIGH} = \frac{R_{BIAS} + R_{ADJ}}{R_{ADJ} + 2 \times R_{BIAS}} \times V_{MAX}.$$

Substituting and solving for  $R_{BIAS}$  and given that the maximum voltage reports a value of 255, the maximum low voltage reports a value of 78, the maximum high voltage value reported is 178, and  $R_{ADJ}$  has a value of 5 k $\Omega$  yield the following equation:

$$R_{BIAS} = \frac{V_{LOW} \times R_{ADJ}}{V_{MAX} - 2 \times V_{LOW}} =$$

$$\frac{R_{ADJ} \times (V_{HIGH} - V_{MAX})}{V_{MAX} - 2 \times V_{HIGH}} = 3875\Omega.$$

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The computed value of  $R_{BIAS}$  is 3875 $\Omega$ . Using a standard value of 3.3 k $\Omega$ , the potentiometer's input ranges from 73 to 182. This range yields a larger dynamic range than you need but allows for a guard range between the potentiometer's values and the push-buttons' values. Because the position of  $R_{ADJ}$  affects the overall resistance the circuit sees when you press either switch, the microcontroller must interpret a range of values for each switch. To determine the switch resistance,  $R_{SW}$ , for either  $S_1$  or  $S_2$ , you use a parallel-resistor network at both extremes of the potentiometer's position.

When you press  $S_1$  and  $R_{ADJ}$  is at the maximum position, the effective resistance of the bottom leg of the divider is  $R_{SW}$  in parallel with the series combination of  $R_{ADJ}$  and  $R_{BIAS}$ . At the minimum position, the effective resistance is  $R_{SW}$  in parallel with  $R_{BIAS}$ :

$$R_{EFFMAX} = \frac{R_{SW} \times (R_{ADJ} + R_{BIAS})}{R_{SW} + R_{ADJ} + R_{BIAS}};$$

$$R_{EFFMIN} = \frac{R_{SW} \times R_{BIAS}}{R_{SW} + R_{BIAS}}.$$

You determine the value when you press  $S_1$  by evaluating the voltage divider that  $R_{BIAS}$  and  $R_{EFFMAX}$  form:

$$V_{SIMAX} = \frac{R_{EFFMAX}}{R_{EFFMAX} + R_{BIAS}} \times V_{MAX}$$

Observe that when  $R_{ADJ}$  is at its maximum value and you press  $S_1$ , it must produce a value less than the smallest value  $R_{ADJ}$  produces by itself to uniquely determine that you have pressed the switch. So the maximum effective resistance,  $R_{EFFMAX}$ , must produce a value less than the maximum low voltage, as the following equation shows:

$$R_{EFFMAX} < \frac{R_{BIAS}^2}{R_{BIAS} + R_{ADJ}}$$

Substituting and solving this equation for the switch resistance yields:

$$R_{SW} < \frac{R_{BIAS}^3 + R_{BIAS}^2 \times R_{ADJ}}{R_{ADJ}^2 + 2 \times R_{ADJ} \times R_{BIAS}}$$

Using the spreadsheet to compute the switch resistance yields 1558 $\Omega$ , and you can choose a nominal 1.5-k $\Omega$  resistor. This selection causes  $S_1$  to produce a range of 28 to 71 when you press it, depending on the potentiometer's position. Likewise, choosing the same value for  $S_2$  produces a range of 184 to 227. These ranges are bands of values that you can use to determine which switch you pressed regardless of the potentiometer's position. Although selecting symmetrical resistor

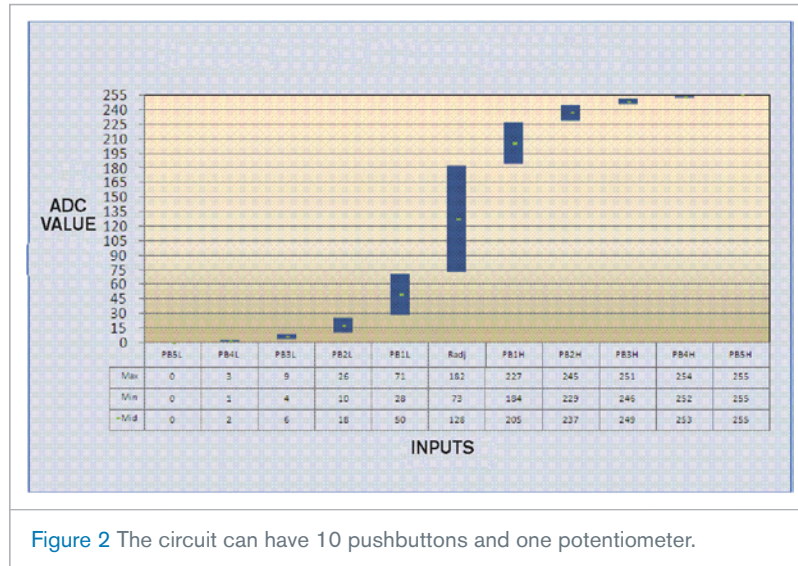


Figure 2 The circuit can have 10 pushbuttons and one potentiometer.

values is not necessary, it minimizes the number of calculations you need to perform and simplifies the design. Furthermore, selecting smaller series switch resistors opens the guard range between them and the potentiometer, which may be desirable if the resulting values are too close together. The microcontroller uses a small subroutine, **Listing 1**, which you can download at [www.edn.com/100422dia](http://www.edn.com/100422dia), to determine both switch positions and the potentiometer's setting.

The limitation of this technique is that you cannot press more than one pushbutton at any time. In addition, the microcontroller can read the potentiometer's position only when you are not pressing any other pushbuttons. This example shows how to use

two pushbuttons, but the number of pushbuttons can vary. Input ranges are available for as many as 10 pushbuttons and one potentiometer, all of which share the same input pin (**Figure 2**). Although the computed ranges do not overlap and are unique, it is doubtful that your ADC hardware can reliably distinguish these bands under all circumstances. Choosing smaller resistor values keeps these bands farther apart, creating a larger guard range.

Using this technique with four pushbuttons and one potentiometer is well within reason. Experimenting with the spreadsheet helps make quick work of determining just the right series-resistor values for each switch and its output range. **EDN**