DIAGRAM AND TABLE NOT IN AGREEMENT

While reading "Microprocessor Microcourse, Part 2" (April 1978) on basic digital logic, I realized that the description for a D-type latch/flip-flop does not match Fig. 13 and the truth table. Which is correct?—*B. Malik, Tehran, Iran.*

The diagram shown in Fig. 13 is correct. However, the Q and $\overline{Q}$ in the truth table in Fig. 13 were accidentally transposed.