SCHEMATIC ERROR

I just received the March issue of Radio-Electronics with my article, "The State-of-the-Art of Doing Nothing." It looks good, but an error did creep into the schematic (Fig. 4, page 62).

Pin 14 of IC4 should be connected to pin 22, regardless of which EPROM is used, the diagram shows it connected to pin 19

The * note is correct, except for the last part. It should read " +5V, for 2716 connect pin 19 of IC5 to pin 15 of IC4 "

The other note should read: "Use pin 15 of IC4 if a 2758 is used, use pin 1 of IC4 if a 2716 is used."

Pin 22 of the EPROM is the A9 line and pin 19 is the A10 line. As drawn, the schematic will only display portions of the EPROMS' programs and will not reset until after several program repeats

There's a minor error in column two on page 62: reset occurs when IC4 reaches either 1024 or 2048. The figure 512 was for the 74S PROM

Otherwise, it looks great.
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