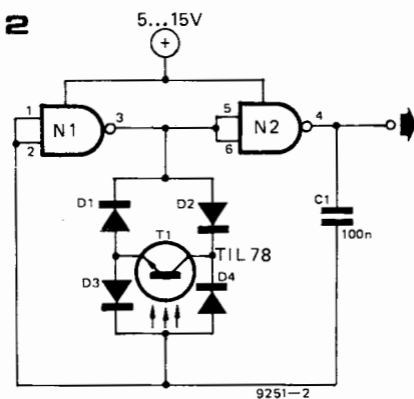
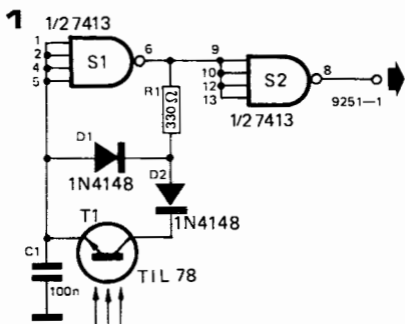


Light Detector



$N1, N2 = \frac{1}{2} \times 4011$

$D1...D4 = 1N4148$

A circuit using CMOS gates and analogous to figure 1 is shown in figure 3. The phototransistor is connected in a feedback loop around N1 and since the phototransistor will be reverse-biased when the output of N1 is low it controls only the half-cycle of the waveform when the output of N1 is high. The duty-cycle therefore varies with light intensity. This can be cured in this circuit by using an LDR (e.g. ORP12)

