

Qume

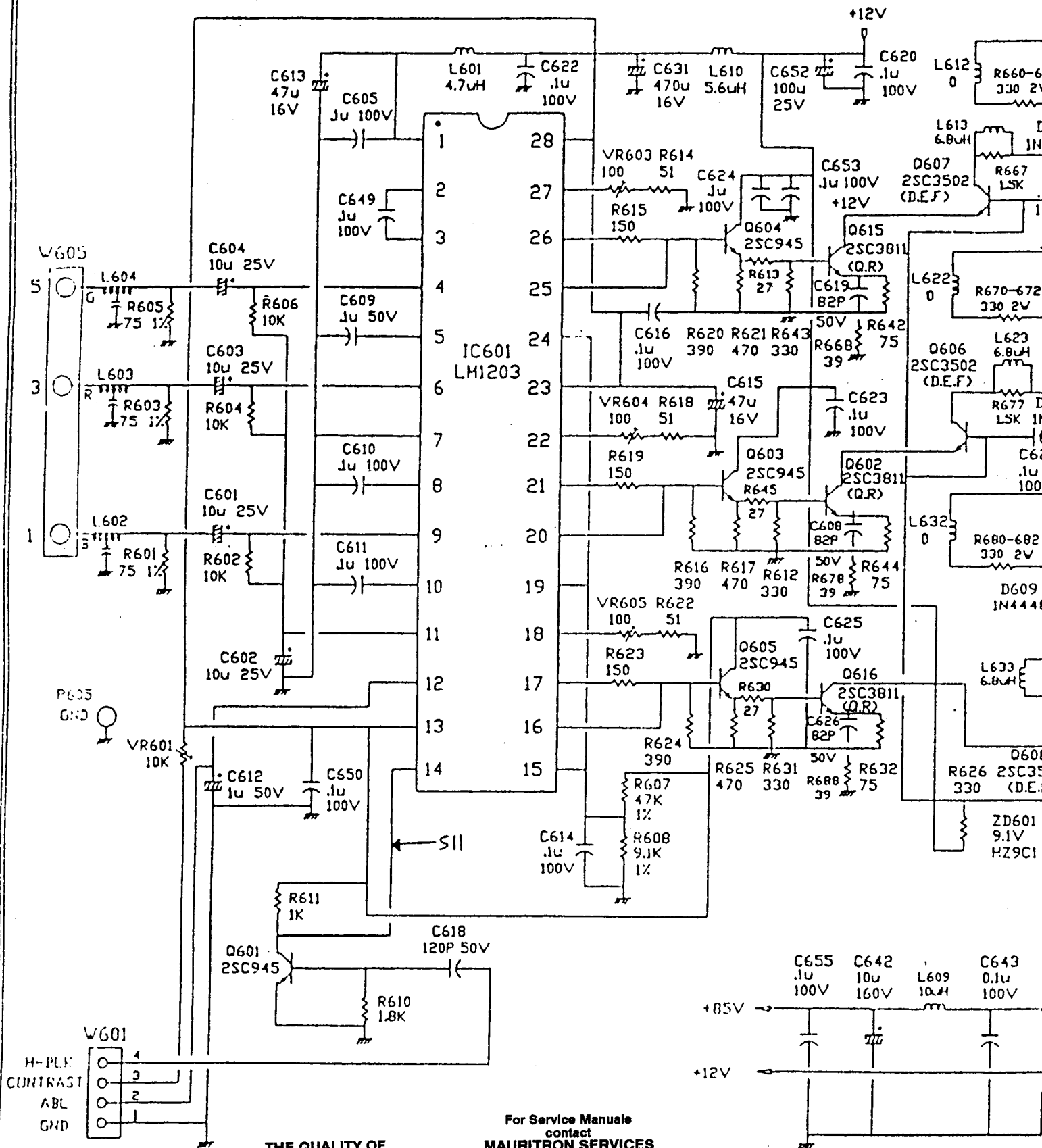
QM 835

Super VGA Color Monitor

MAINTENANCE GUIDE

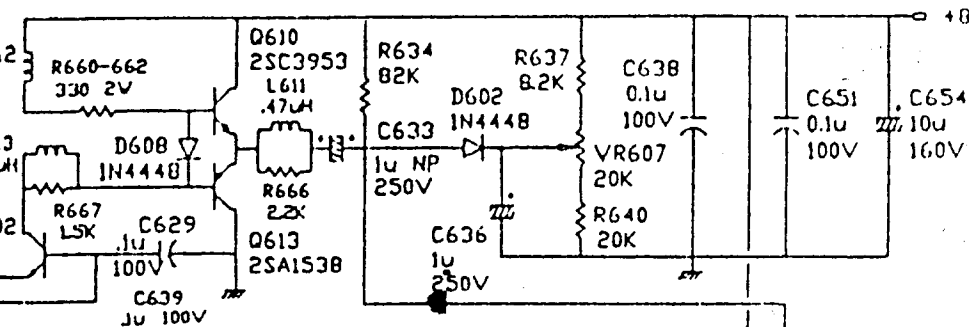
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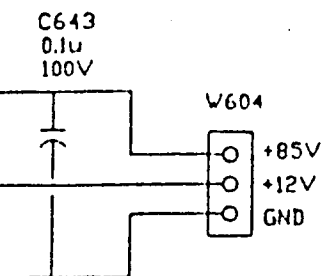
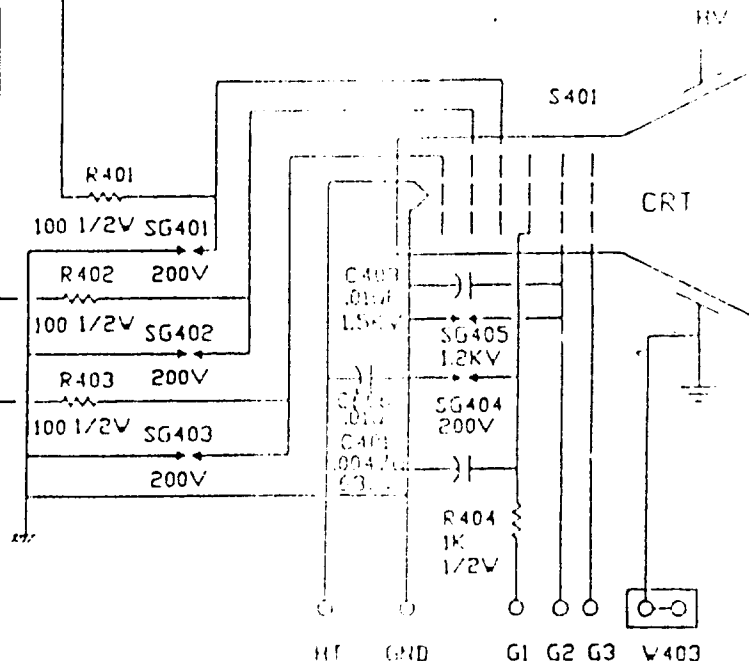
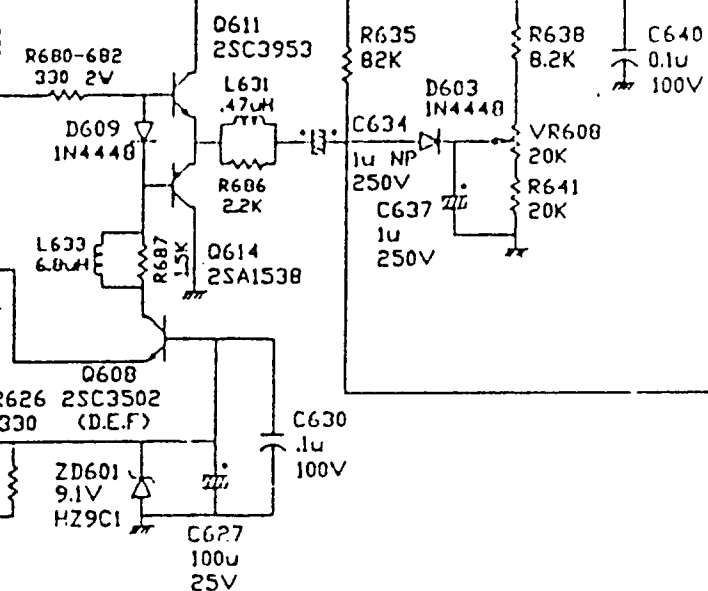
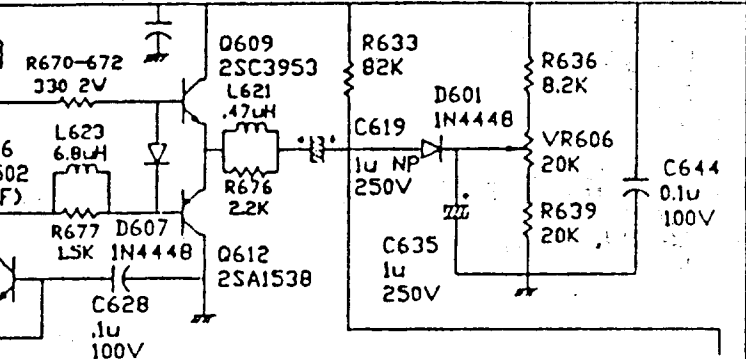
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NAME: QM-835

PART: VIDEO

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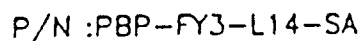
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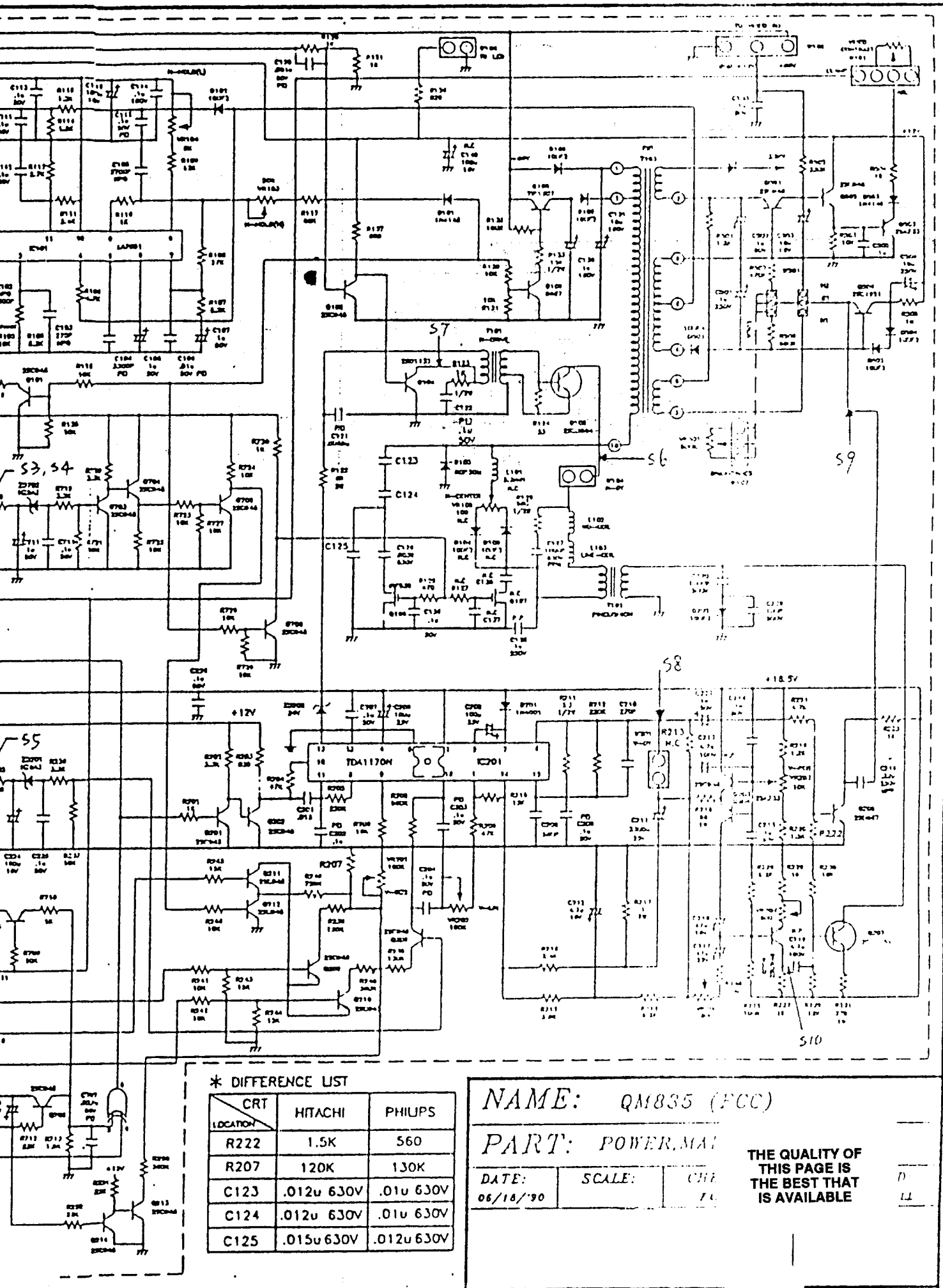
NOTE:

ASV-IDE-002-00
0145V-C-V

PRIMARY SEC







* DIFFERENCE LIST

CRT LOCATION	HITACHI	PHILIPS
R222	1.5K	560
R207	120K	130K
C123	.012u 630V	.01u 630V
C124	.012u 630V	.01u 630V
C125	.015u 630V	.012u 630V

NAME: QM835 (FCC)

PART: POWER.MA1

DATE: 06/16/90

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QM 835 ALIGNMENT PROCEDURE

1. DC VOLTAGE ADJ.

- a. Input VGA 640x480 Crosshatch pattern signal.
- b. Adj. W102 (Main B.) 85V point DC voltage to $85V \pm 0.5V$ by **VR301**
- c. Adj. P101 (ZD702 negative point) DC voltage to $9.65 \pm 0.05V$ by **VR701**.

2. H-HOLD, H-PHASE ADJ.

- a. Input VGA 640x480 Crosshatch pattern signal.
- b. Short P102 (**IC101 pin 1**) to ground and adj. the picture to approach holding by **VR104**.
- c. Remove P102 to ground and make sure the picture be held.
- d. Adj. the picture to center of background by **VR102** (External H-phase control)
- e. Input 8514 Mode (1024x768) Crosshatch pattern signal.
- f. Short P102 (**IC101 pin 1**) to ground and make sure the picture to approach holding by **VR103**.
- g. Remove P102 to ground and make sure the picture be held.
- h. Adj. the picture to center of background by **VR101**.

3. V-SIZE, V-LIN, V-POSITION, H-WIDTH, PINCUSHION ADJ.

- a. Input VGA 640x480 Crosshatch pattern signal.
- b. Adj. V-Size to $180 \pm 3mm$ by **VR201** (External V-size control).
- c. Adj. vertical linearity by **VR202**
- d. Adj. the picture vertical position to center by **VR203** (external V-position control)
- e. Adj. left and right side PINCUSHION to max. 1mm by **VR204** and **VR205** (Pincushion phase and gain).
- f. Adj. H-WIDTH to $245 \pm 3mm$ by **L102**.

4. WHITE BALANCE AND LIGHT OUTPUT ADJ. (By colour analyser)

- a. Input VGA 640x480 1/4-Window-White-Pattern signal.
- b. Turn brightness VR to max. light and contrast VR to min. light (video disappear).
- c. Turn VR606, VR607, VR608 (R.G.B. BIAS, Video B.) to min. light and FBT SCREEN VR to background just appear. Adj. Background white balance to $x=281 \pm 10\%$, $y=311 \pm 10\%$, $Y=0.4 \pm 0.3FL$ by **VR606**, **VR607**, **VR608** and **SCREEN VR**.
- d. Turn Contrast VR to max. light and VR603, VR604 VR605 to max. light and brightness VR to about 6FL () light. Adjust Video White Balance to $x=281 \pm 10\%$, $y=311 \pm 10\%$. Turn brightness VR to max light. Adj. light output to $75 \pm 5FL$ by **VR601** (SUB-CONTRAST, Video B.)

5. FOCUS ADJ.

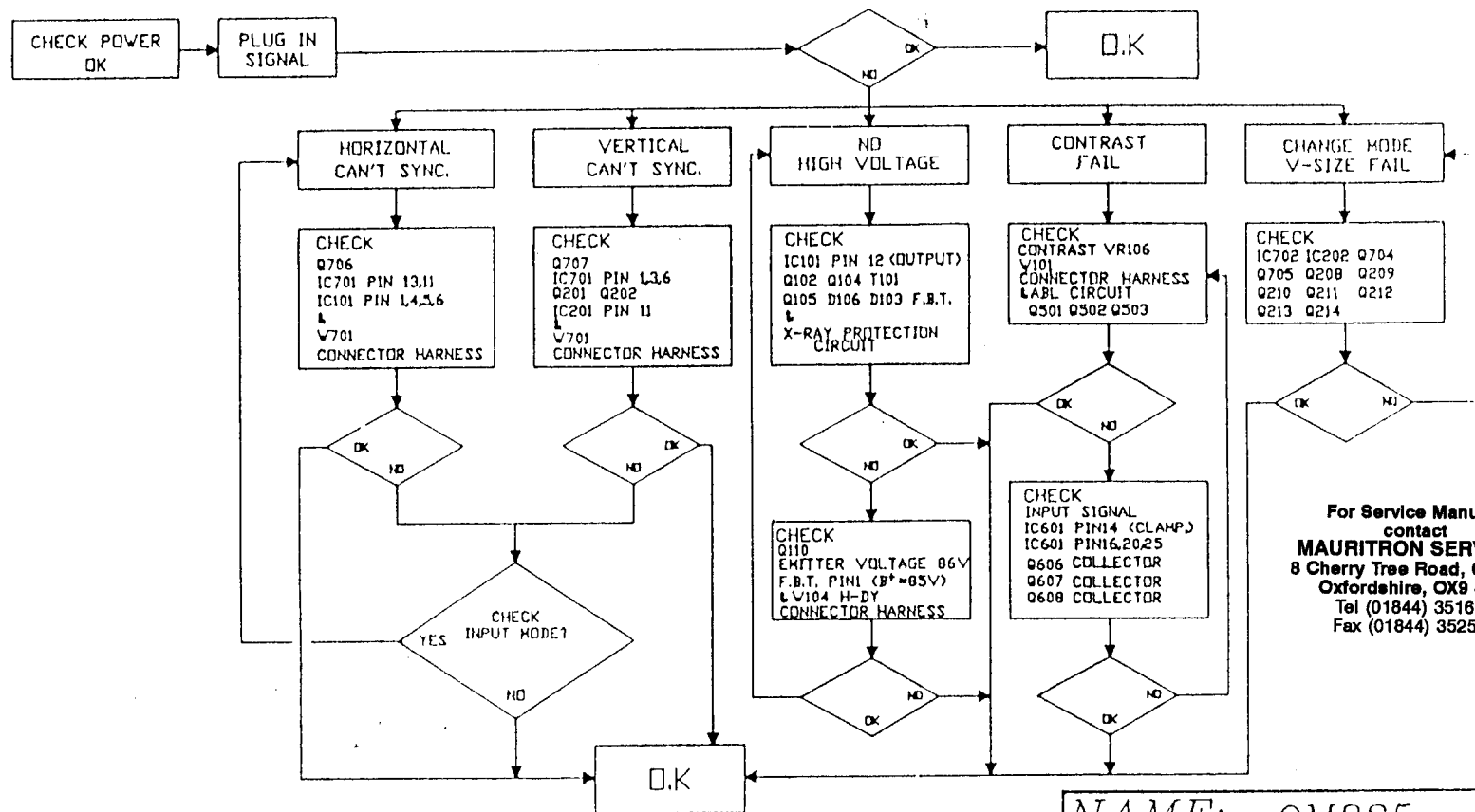
- a. Input 8514 Mode (1024x768) H-Character-Pattern signal.
- b. Adj. picture center and corner focus to clear by **FBT FOCUS VR** (max. light).

6. CONVERGENCE ADJ.

- a. Input 8514 Mode (1024x768) Crosshatch Purple pattern signal.
- b. Adj. RED and BLUE misconvergence by YOKE 4 POLE.
- c. Input 8514 Mode (1024x768) Crosshatch Yellow pattern signal.
- d. Adj. RED and GREEN misconvergence by YOKE 6 POLE.
- e. Input 8514 Mode (1024x768) Crosshatch Purple pattern signal.
- f. Check convergence in spec.
- g. Check FOCUS again.

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Note:	1.	VGA 640x350	Timing:	Fh 31.47kHz,	Fv: 70Hz
	2.	VGA 640x400	Timing:	Fh 31.47kHz,	Fv: 70Hz
	3.	VGA 640x480	Timing:	Fh 31.47kHz,	Fv: 60Hz
	4.	VGA 1024x768	Timing:	Fh 35.52kHz,	Fv: 87Hz (Interl.)



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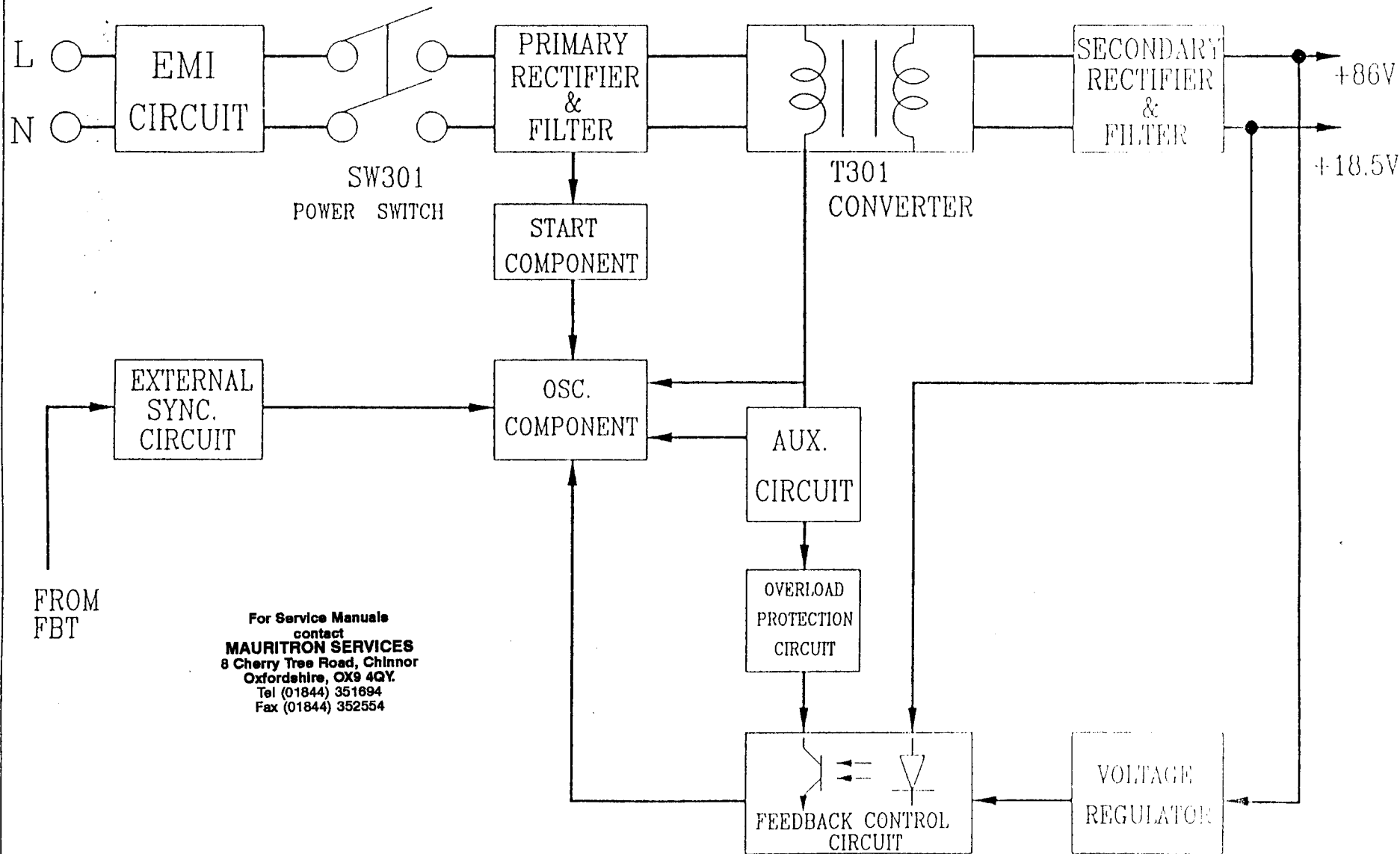
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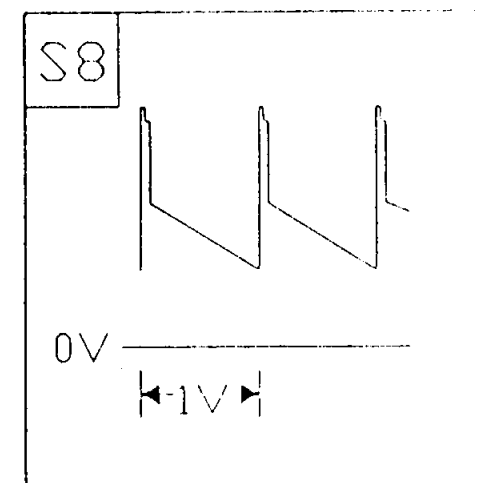
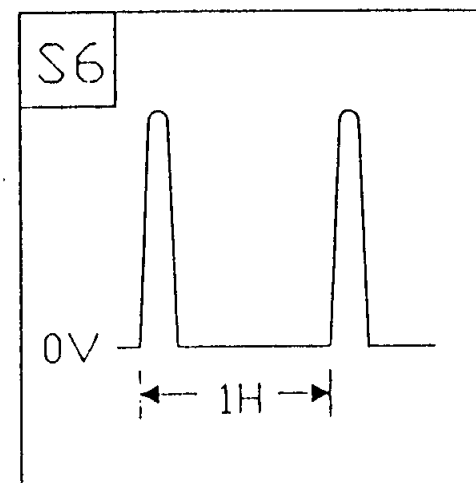
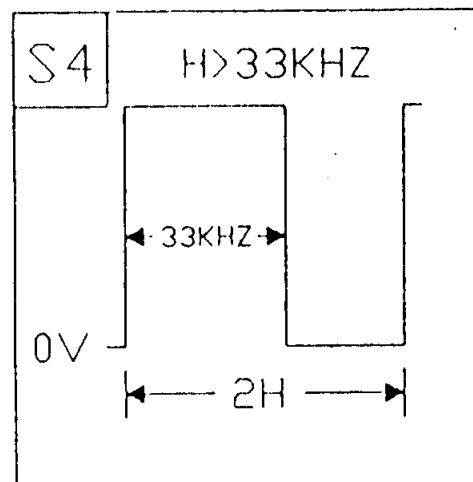
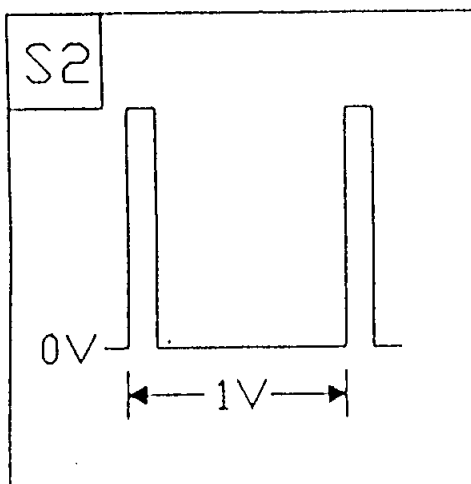
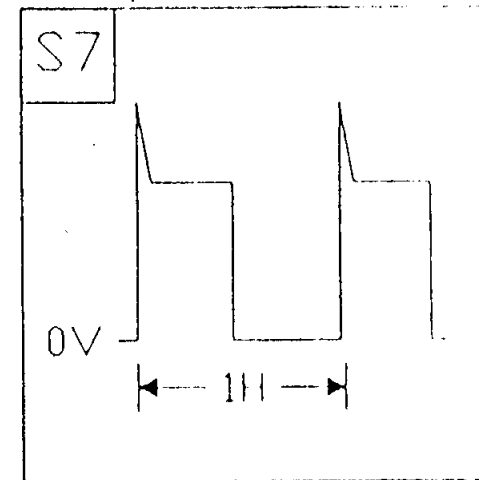
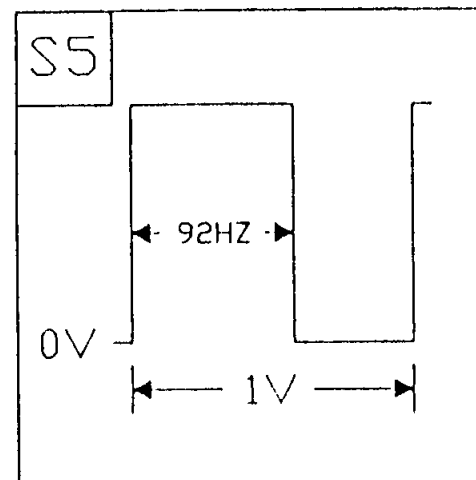
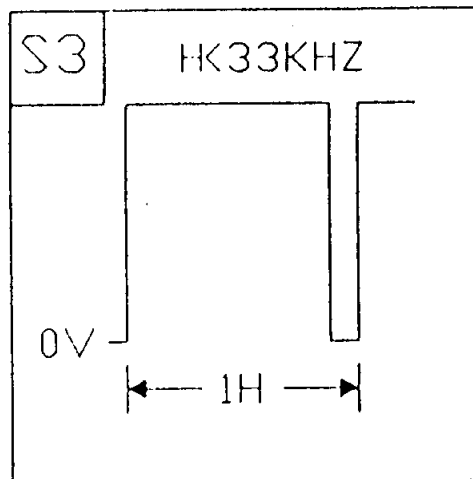
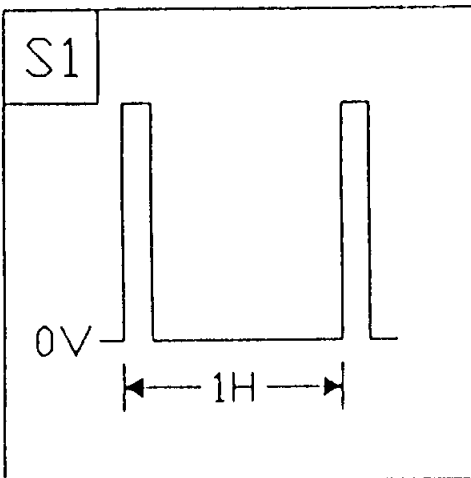
PART: TROUBLE SHOOTING GUIDE

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NOTE:



QM835 S.M.P.S BLOCK DIAGRAM



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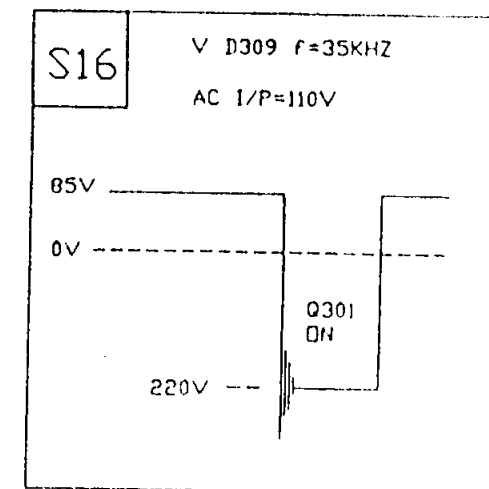
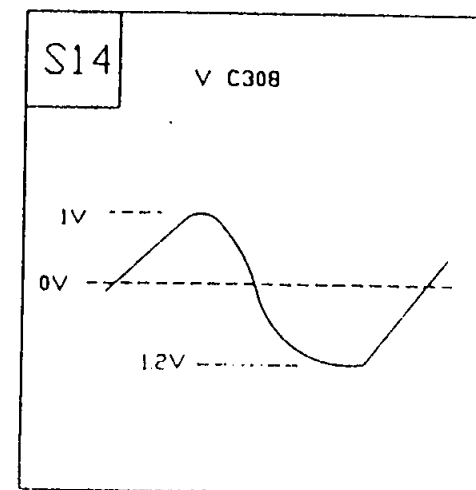
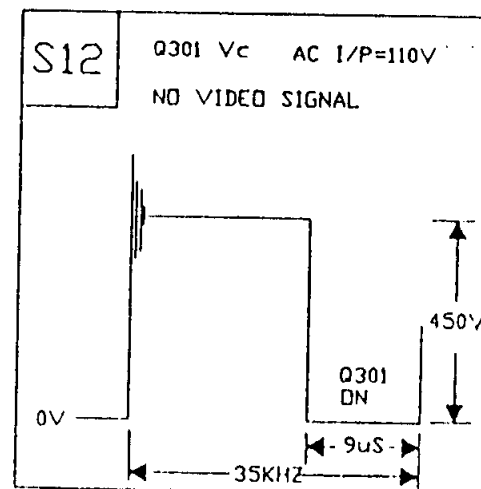
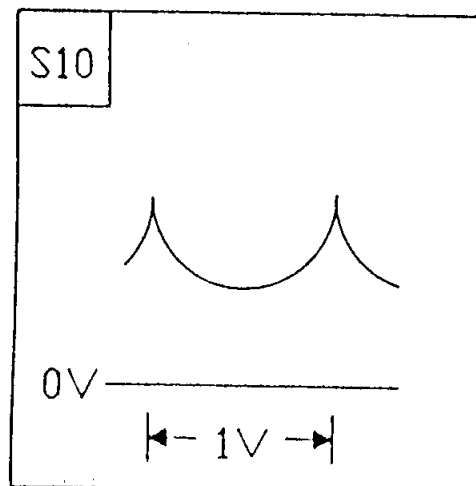
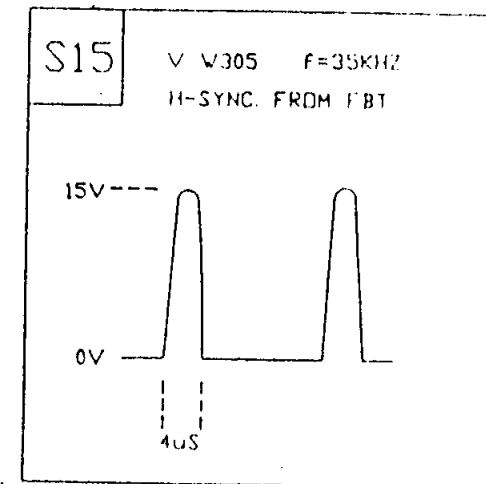
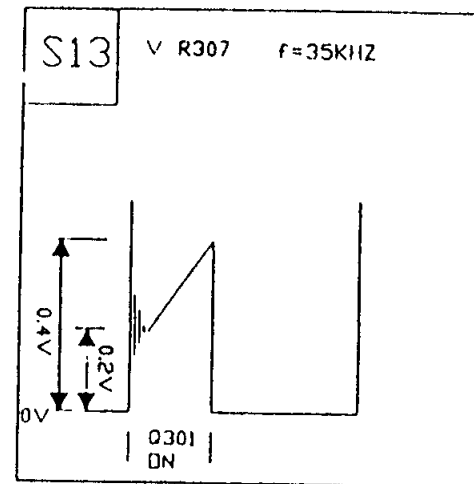
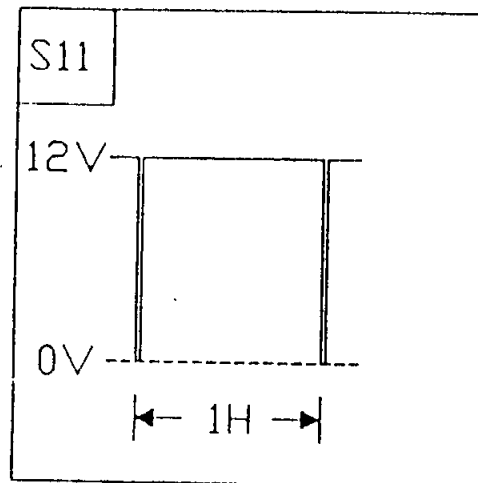
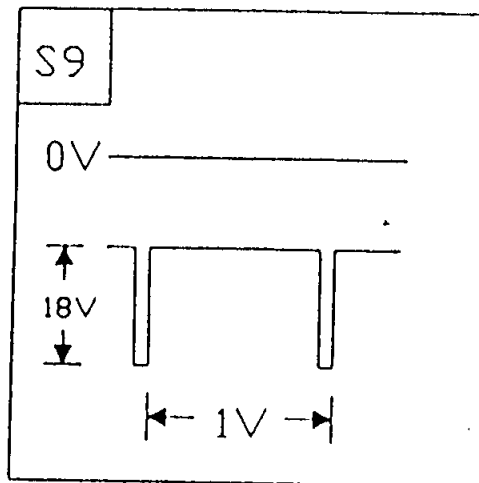
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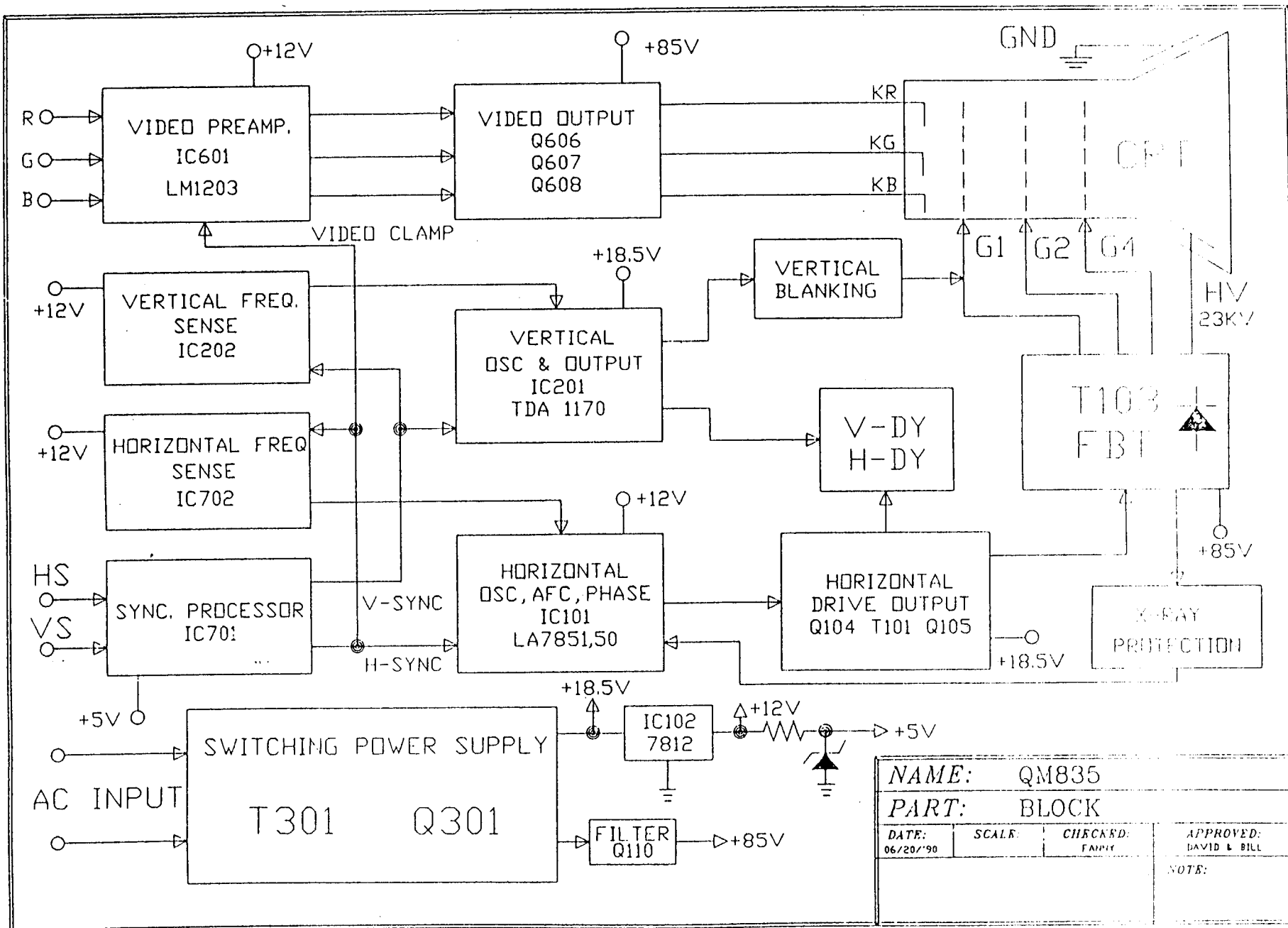
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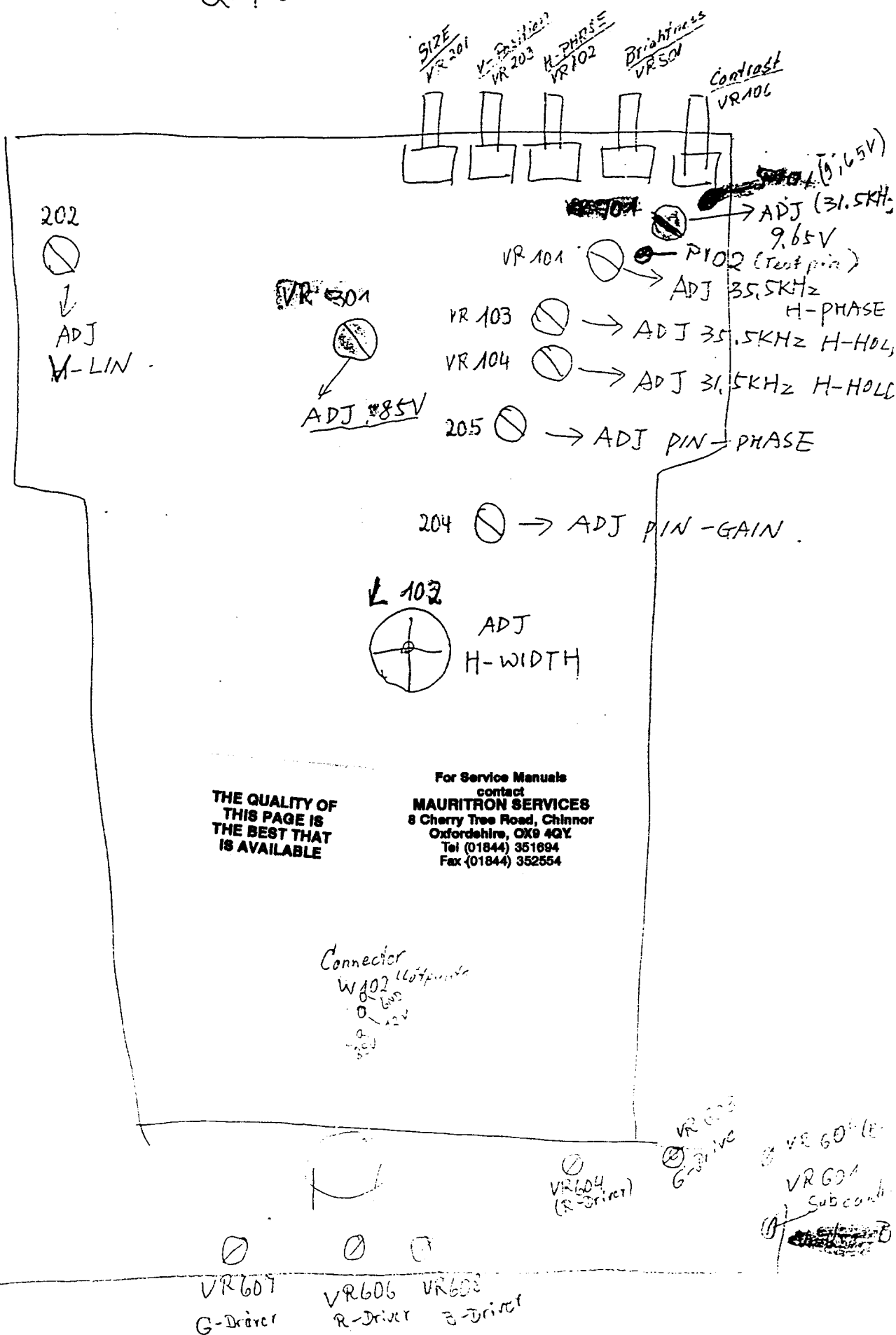
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